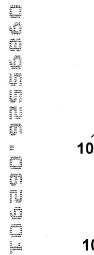
i () ;



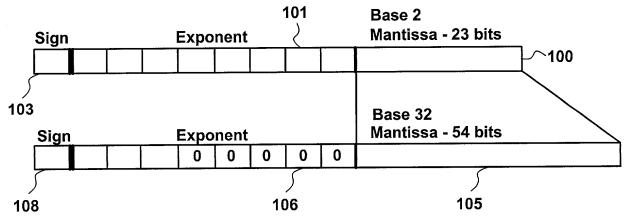


Fig. 1

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Conditioning Accumulator Inputs

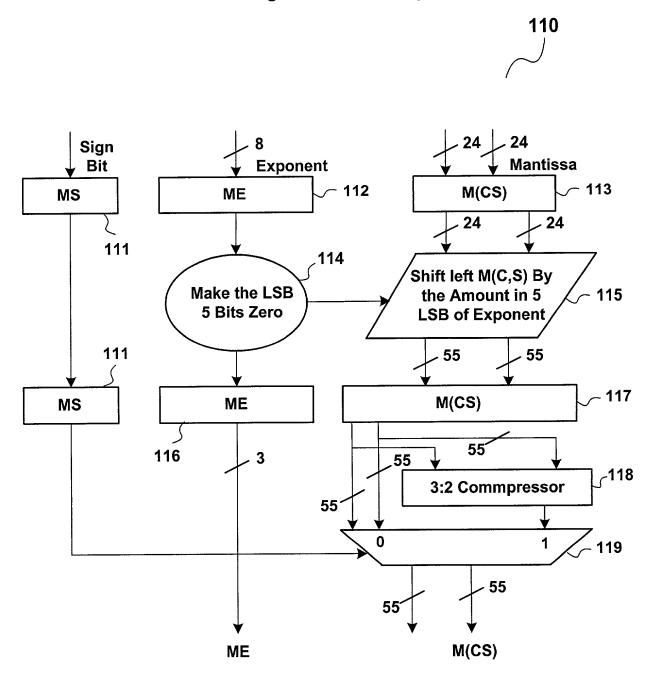


Fig. 2

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## **Exponent Loop**

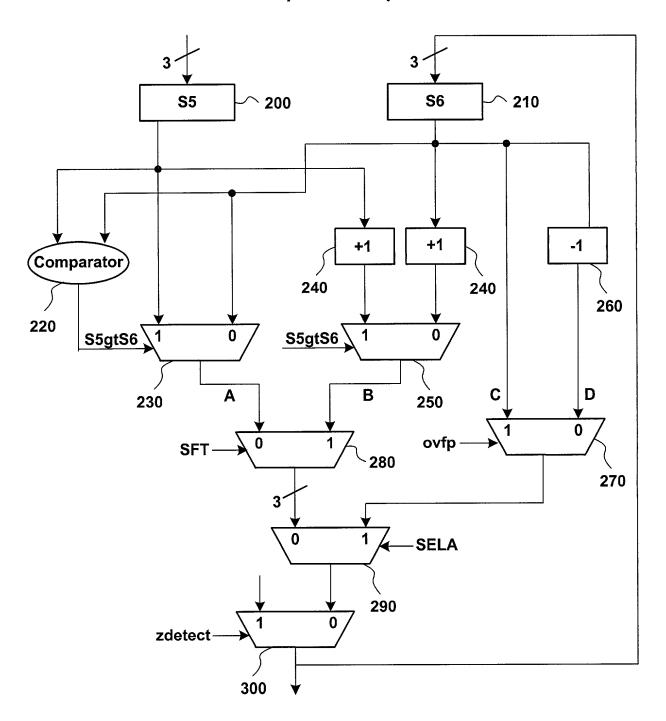


Fig. 3

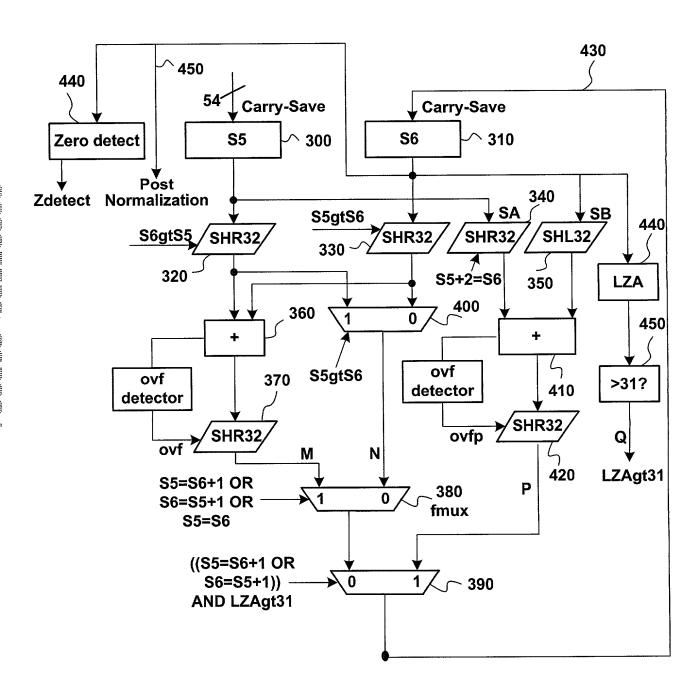


Fig. 4

## **Postnorm**

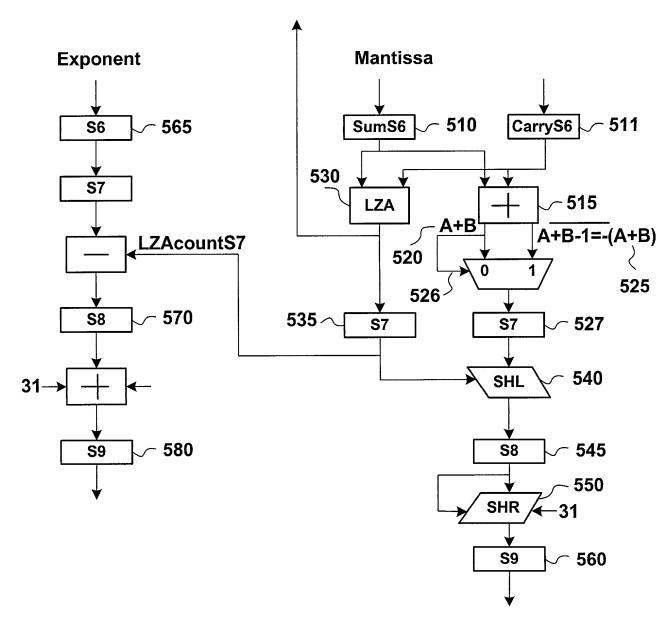


Fig. 5